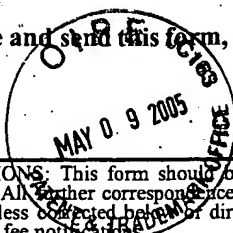


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Terri J. Benders	(Depositor's name)
<i>Terri J. Benders</i>	(Signature)
May 8, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/761,550	01/21/2004	Jack Hwang	ITL.0945US (P15969)	2845

TITLE OF INVENTION: CREATING SHALLOW JUNCTION TRANSISTORS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/06/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGO, NGAN V	2818	438-299000

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- ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

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Trop, Pruner & Hu, P.C.

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Intel Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Santa Clara, California

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Timothy N. Trop

May 5, 2005

Date

28,994

Typed or printed name

Registration No.

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